L Number	Hits	Search Text	DB	Time stamp
1	3	(refresh adj transistor) and (gate adj capacitance)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28
2	415	refresh\$ and (gate adj capacitance)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28
3	233	<pre>(refresh\$ and (gate adj capacitance)) and (word adj line\$1)</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:34
4	194	((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:34
5	20	(((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and periodic	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:41
6	31	(((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and (address adj decoder)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:43
7	127	(((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:44
8	0	((((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM) and (on adj status)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:44
9	12	((((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM) and (switching adj transistor)	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:46
10	127		USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:46
11	0	(((((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM) and bit) and deocder	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:46
12	90	(((((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM) and bit) and decoder	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:47
13	83	((((((refresh\$ and (gate adj capacitance)) and (word adj line\$1)) and (bit adj line\$1)) and DRAM) and bit) and decoder) and charge\$1	USPAT; EPO; JPO; DERWENT; USOCR	2004/08/28 13:48